# **MS T3161**





# **Key Parameters**

VDRM / VRRM = 1600V = 3160AI<sub>T(AV)</sub> = 63kAITSM  $V_{T(TO)}$ = 0.83V $= 0.082 m\Omega$ rт

### **Features**

- Full blocking capability over wide temperature
- High Surge current capability
- Hermetic metal case with ceramic insulator

- Applications
  Medical Equipment
- **UPS**
- **Power Supplies**
- Motor control
- **Controlled Rectifiers**
- Transportation
- Induction Heating

# **Ordering Information**

MS T	3161	C	XX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code MS T3161C16: 1600V VDRM, VRRM, 26mm clamp height capsule thyristor			

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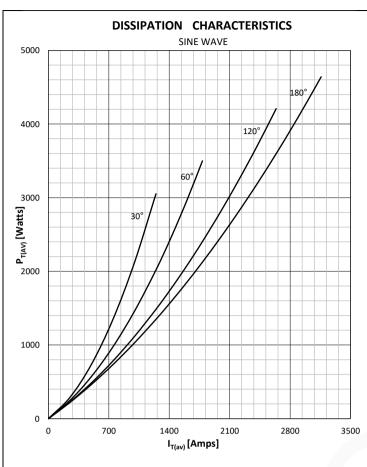


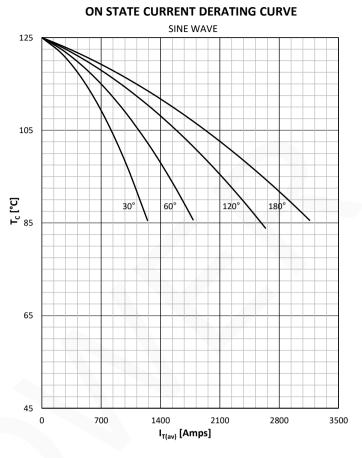
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	1200 - 1600	V
V RSM	Non-repetitive peak reverse voltage		125	1300 - 1700	V
V DRM	Repetitive peak off-state voltage		125	1200 - 1600	V
I RRM	Repetitive peak reverse current	V= V RRM	125	250	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	250	mA
CONDU	CTING		II.		
I T (AV)	Mean on state current	180° sin ,50 Hz, $T_c$ =85°C, Double side cooled 180° sin ,50 Hz, $T_c$ =70°C, Double side cooled		3160 3934	А
I RMS	RMS on-state current	T <sub>c</sub> =85°C, Double side cooled		4961	А
		Sine wave. 10 ms	25	63000	А
I TSM	Surge on-state current	Without reverse voltage	125	57000	Α
	I² t	Sine wave, 10 ms Without reverse voltage	25	19845 x 10 <sup>3</sup>	A²s
I² t			125	16245 x 10 <sup>3</sup>	A²s
Vт	On-state voltage	On-state current = 6000A	125	1.35	V
V T(TO)	Threshold voltage		125	0.83	V
rт	On-state slope resistance		125	0.082	mΩ
OMITOLI					
SWITCH di/dt	Critical rate of rise of on-state current Non-repetitive (f=1Hz)	From 75%V <sub>DRM</sub> , Gate 10V 5Ω, di <sub>G</sub> /dt≥1A/ μs	125	200	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	1000	V/µs
GATE				1	
I gt	Gate trigger current	V <sub>D</sub> =6V	25	300	mA
V gt	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	300	mA
I L	Latching current	V <sub>D</sub> =6V	25	1500	mA
MOUNTI	NG		•	· ·	
R th(j-c)	Thermal impedance, Sin180°	Junction to case, Double side cooled		0.0085	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.0097	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.0025	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			40 - 50	kN
W	Weight (Approx.)			1150	gm

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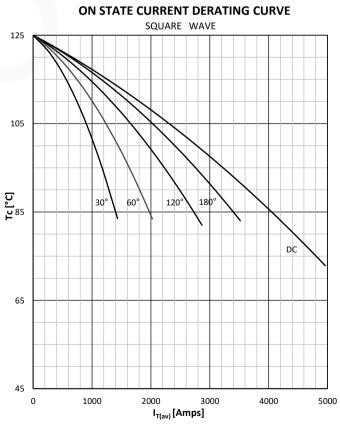
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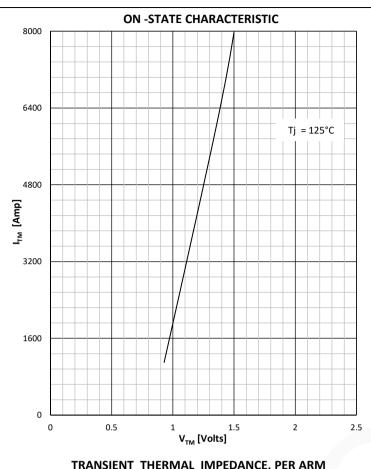
# DISSIPATION CHARACTERISTICS SQUARE WAVE 5200 180° 120° 1300 1300 1300 1300 1400 5000 1400 5000

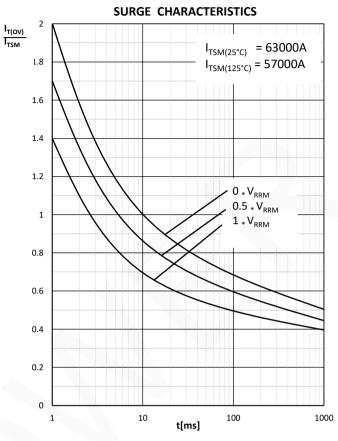


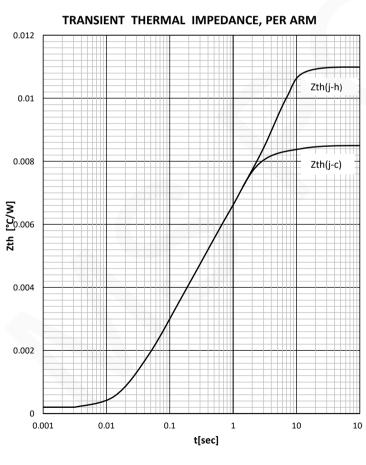
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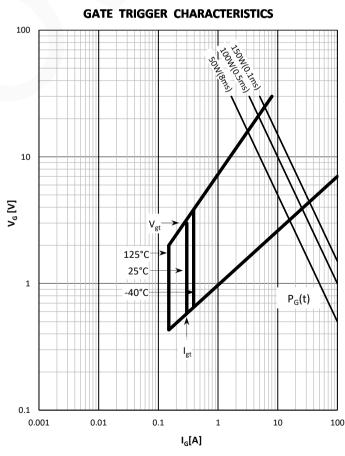
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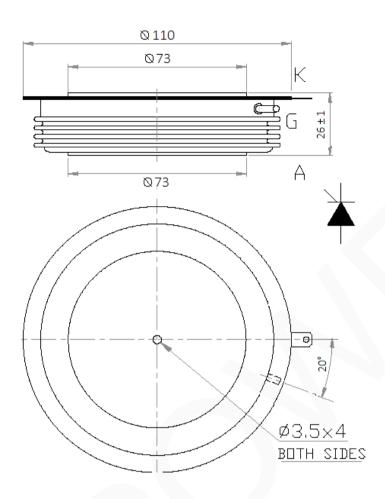
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## Outline



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